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CJ C

PATENT Attorney's Do. No. 5038-265

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent of:

Neeb

Patent No.:

US 6,894,523 B2

Issued:

May 17, 2005

For:

SYSTEM AND METHOD FOR TESTING SEMICONDUCTOR

DEVICES

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

LETTER

In checking the subject original Letters Patent with our copy of the application, the following errors which occurred in the printing of the patent were noted:

Column 7, line 12, "comprising;" should read --comprising:--

Column 7, line 16, "locator the interface" with --located on the interface-Since these are minor errors, we are not requesting the issuance of a Certificate of
Correction. However, it is requested that this letter be made of record in the file of the
patent.

Respectfully submitted,

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Date: October 24, 2006